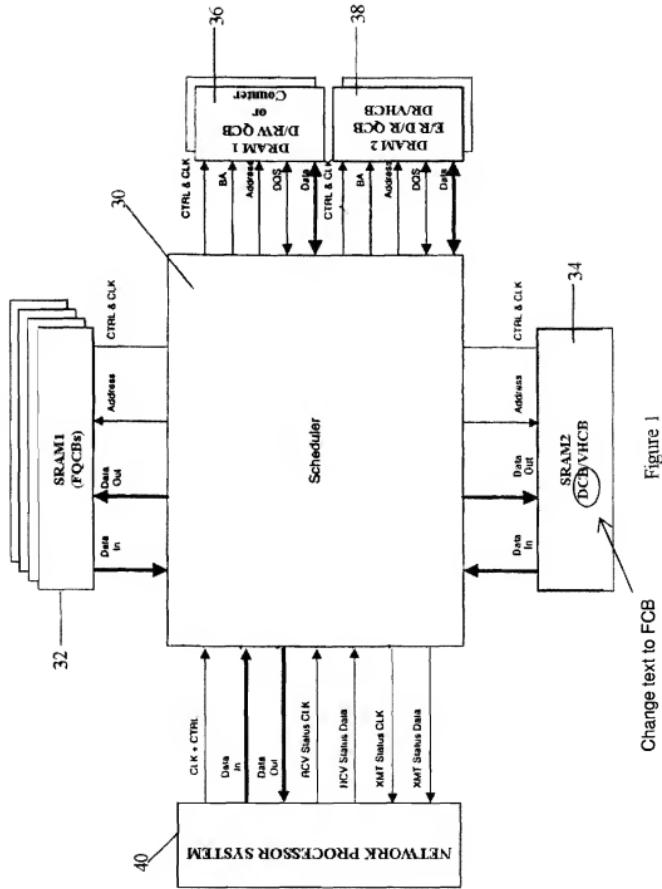


# ANNOTATED SHEET

Sheet 1/9  
Basso et al.  
RPS920030157/US1

## Hardware Scheduler Memory Arrangement



# ANNOTATED SHEET

Sheet 2/9  
Basse et al.  
RPS920030157US1

## Hierarchical Link Resource Sharing (Variable packet size model)

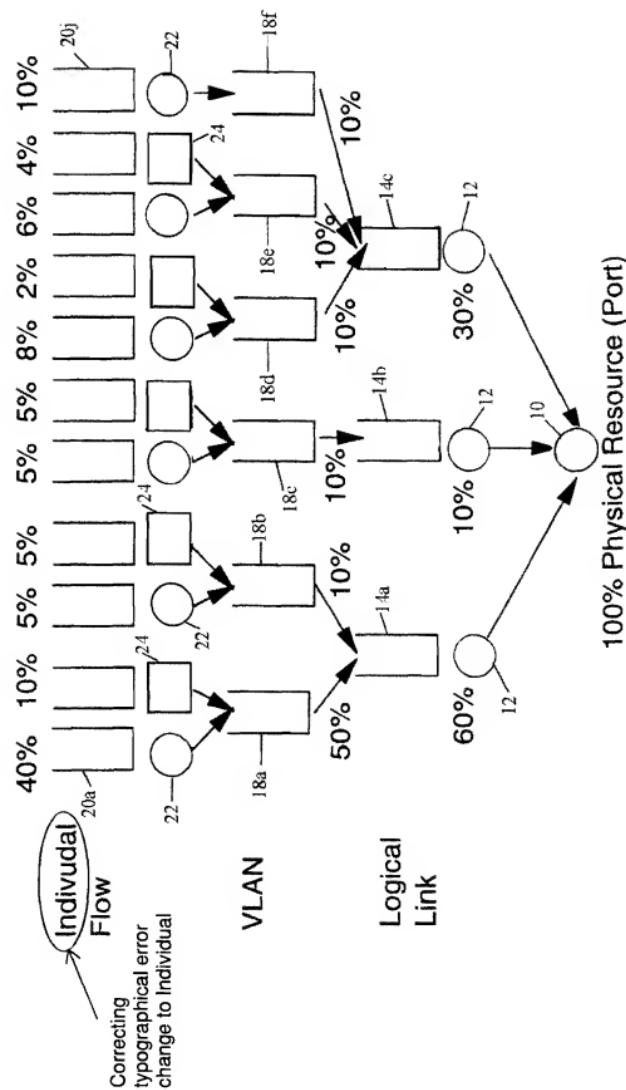


Figure 2